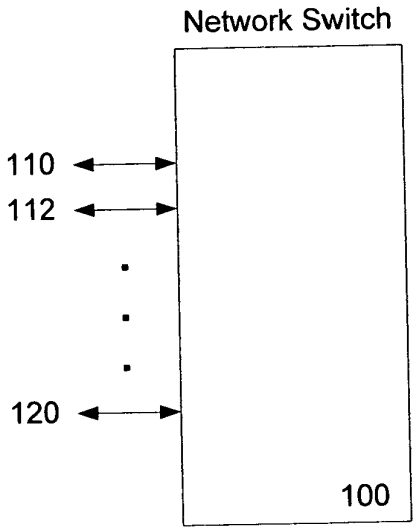

Figure 1A

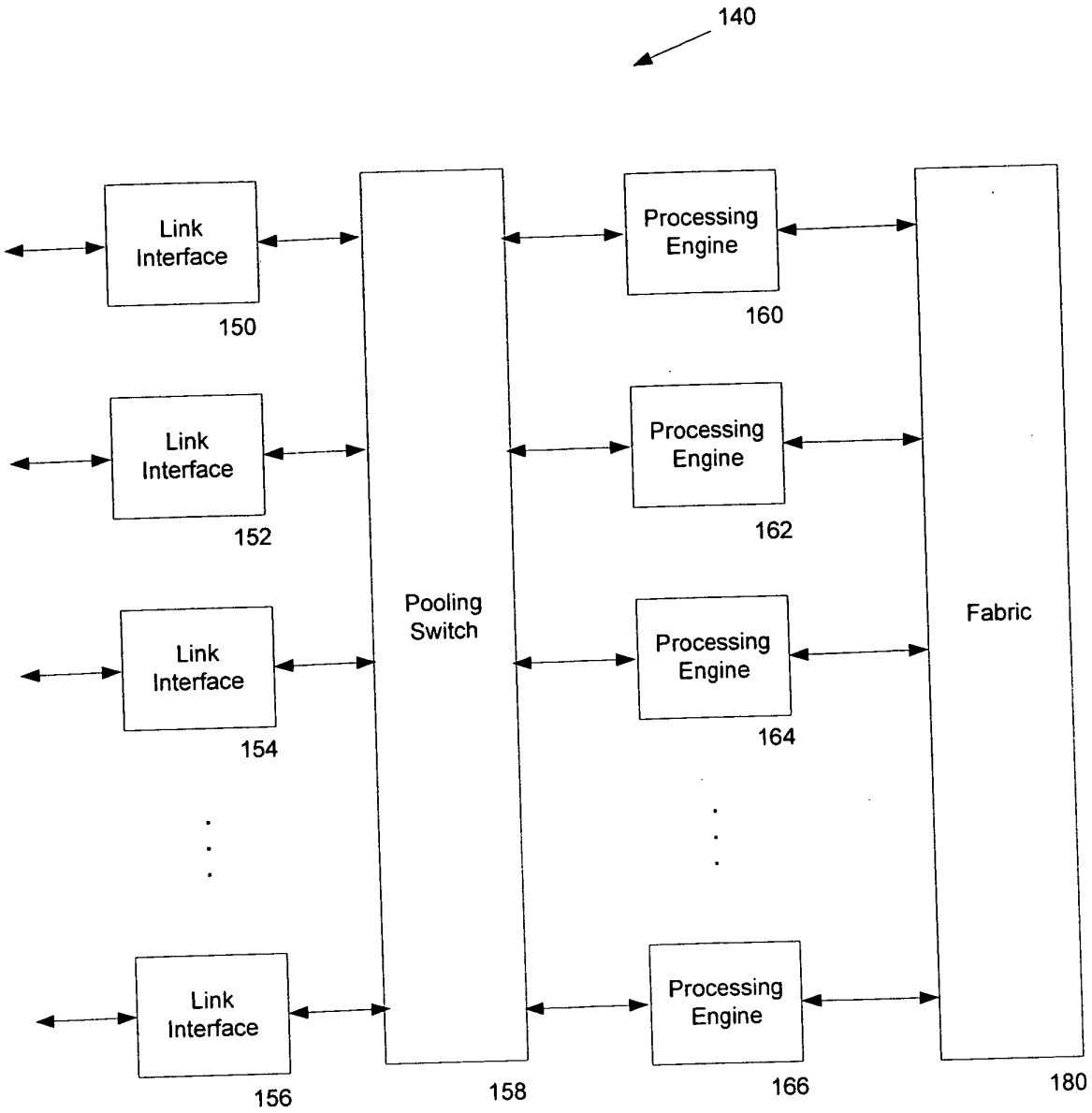
140
Link Interface
150
Link Interface
152
Link Interface
154
Link Interface
156
Pooling Switch
158
Processing Engine
160
Processing Engine
162
Processing Engine
164
Processing Engine
166
Fabric
180

Figure 1B

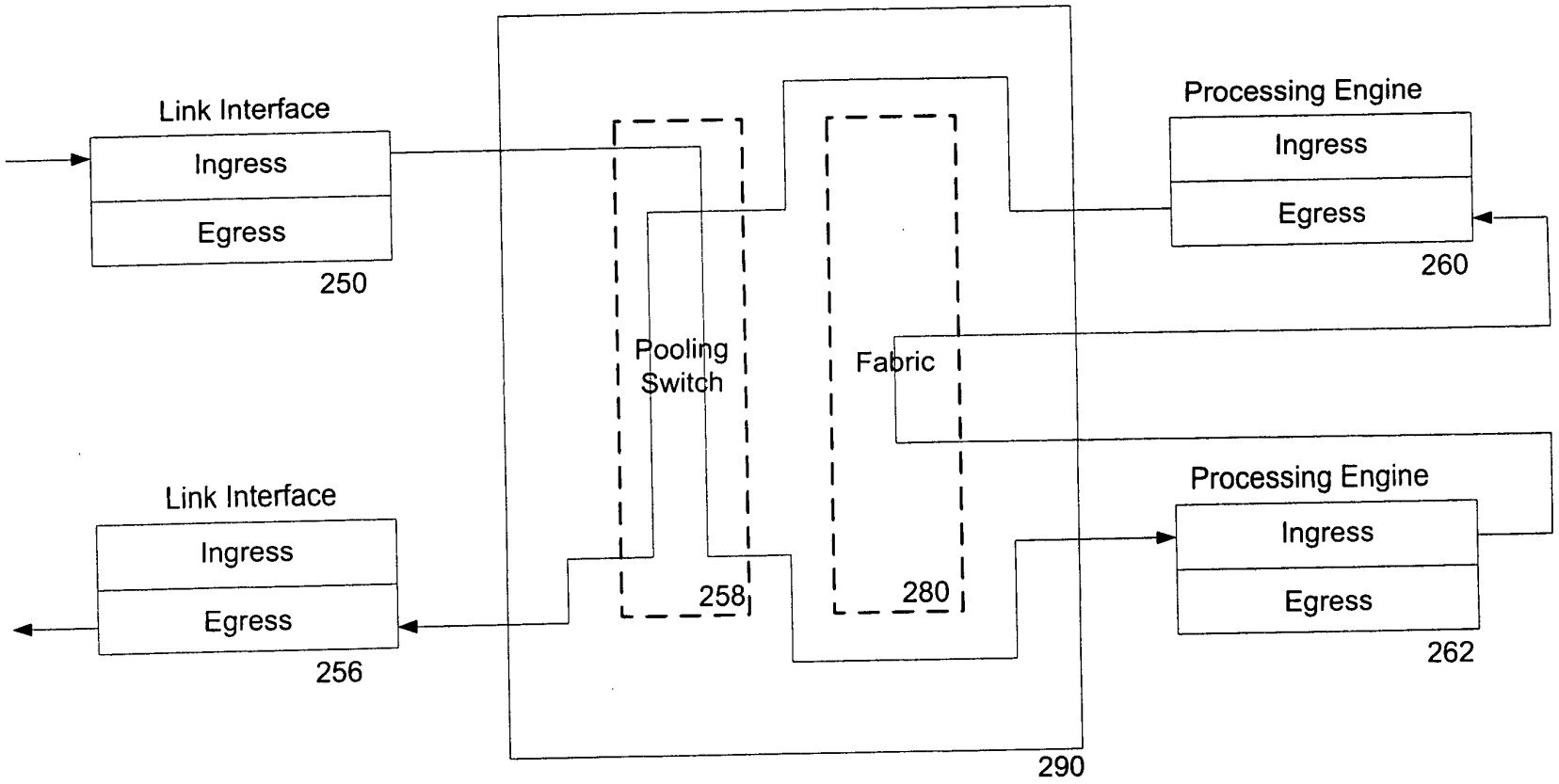

Figure 2 - Physical Data Path Diagram

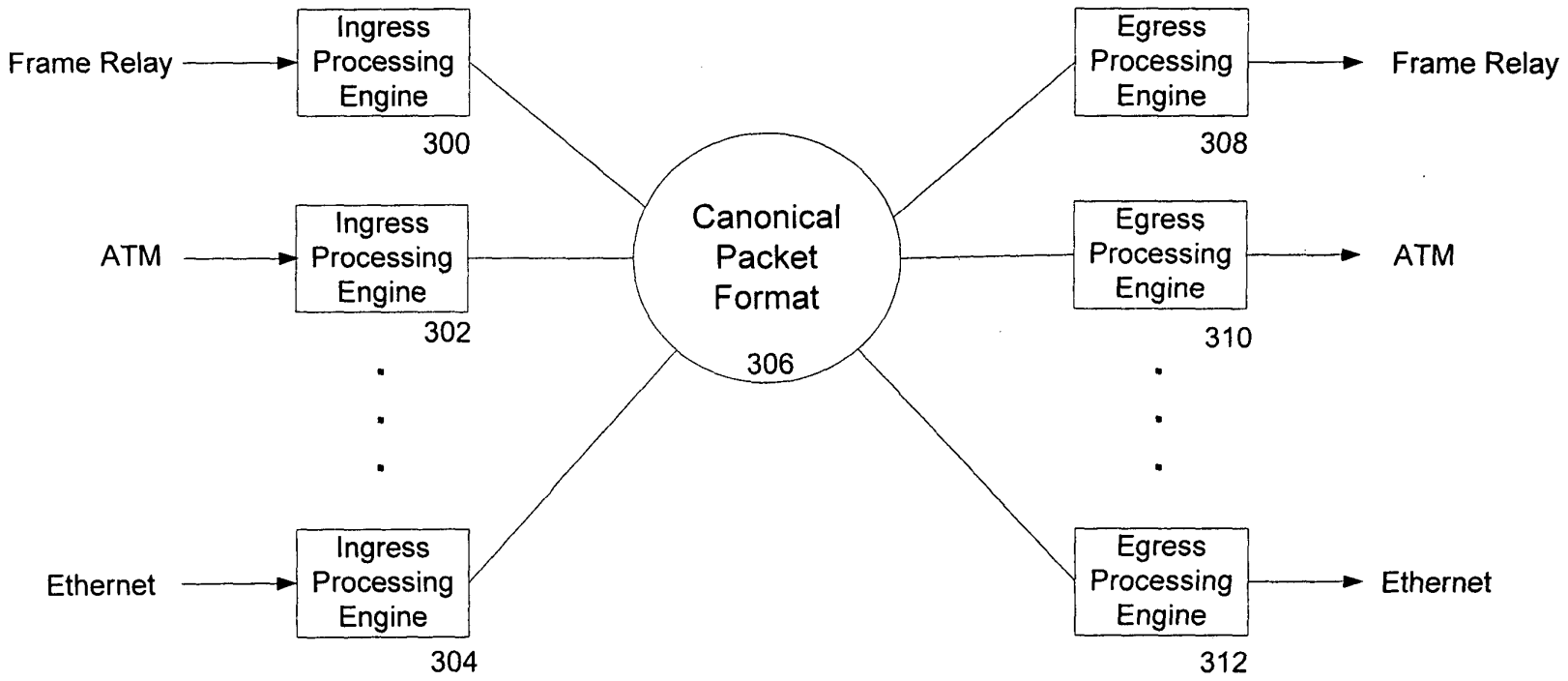

Figure 3A - Logical Protocol Translation Diagram

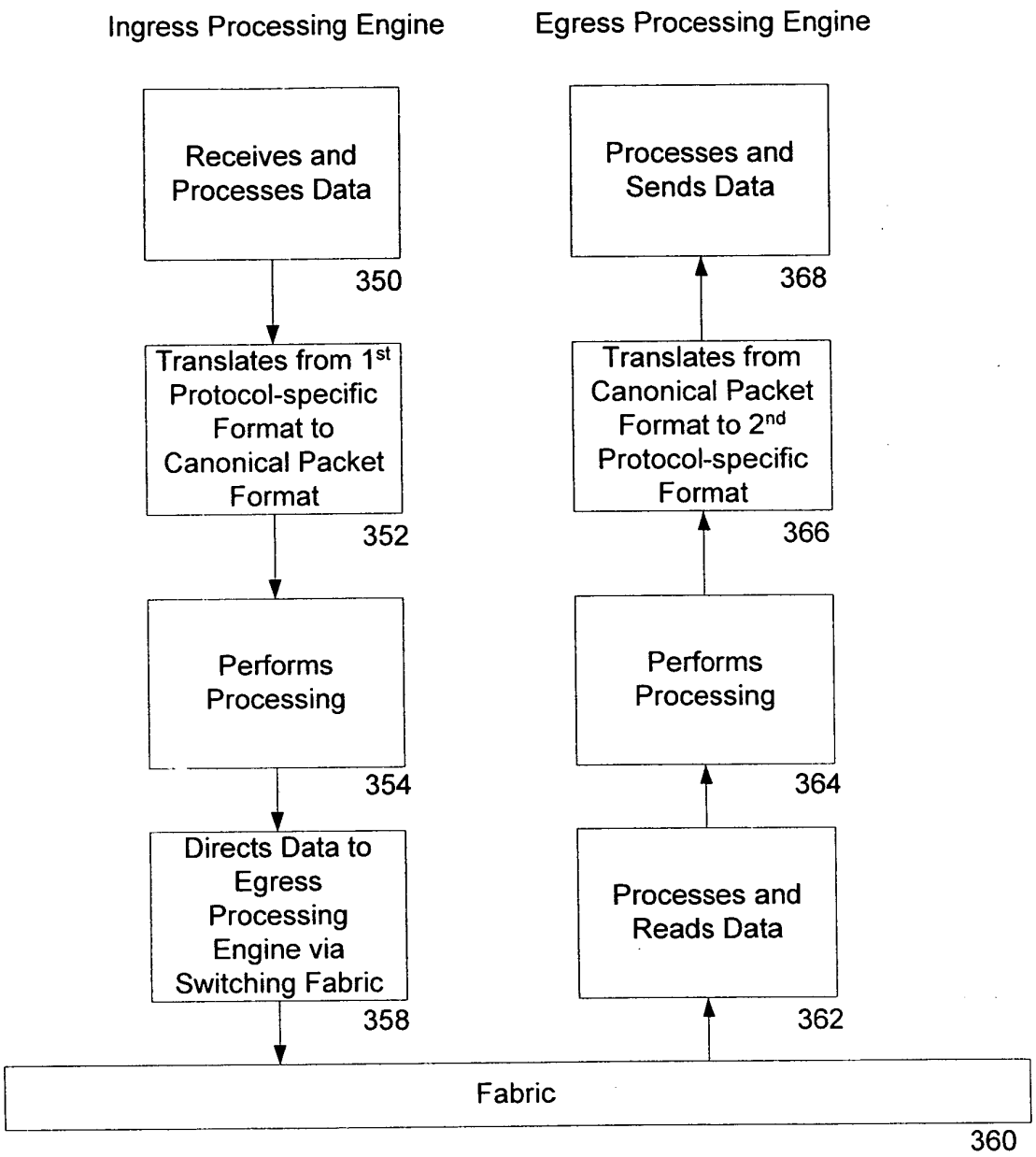

Figure 3B - Translation Function Diagram

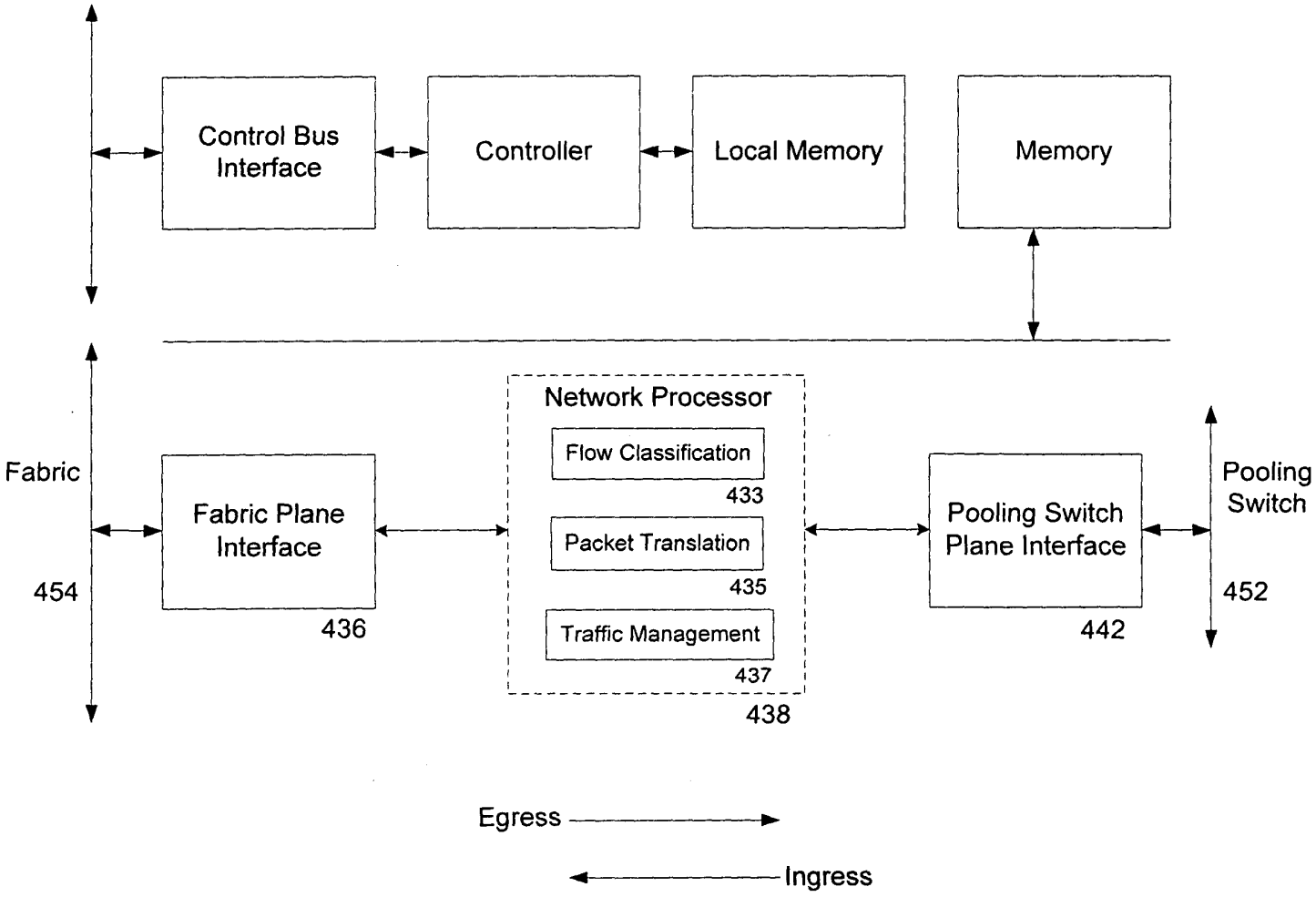

Figure 4 - Processing Engine

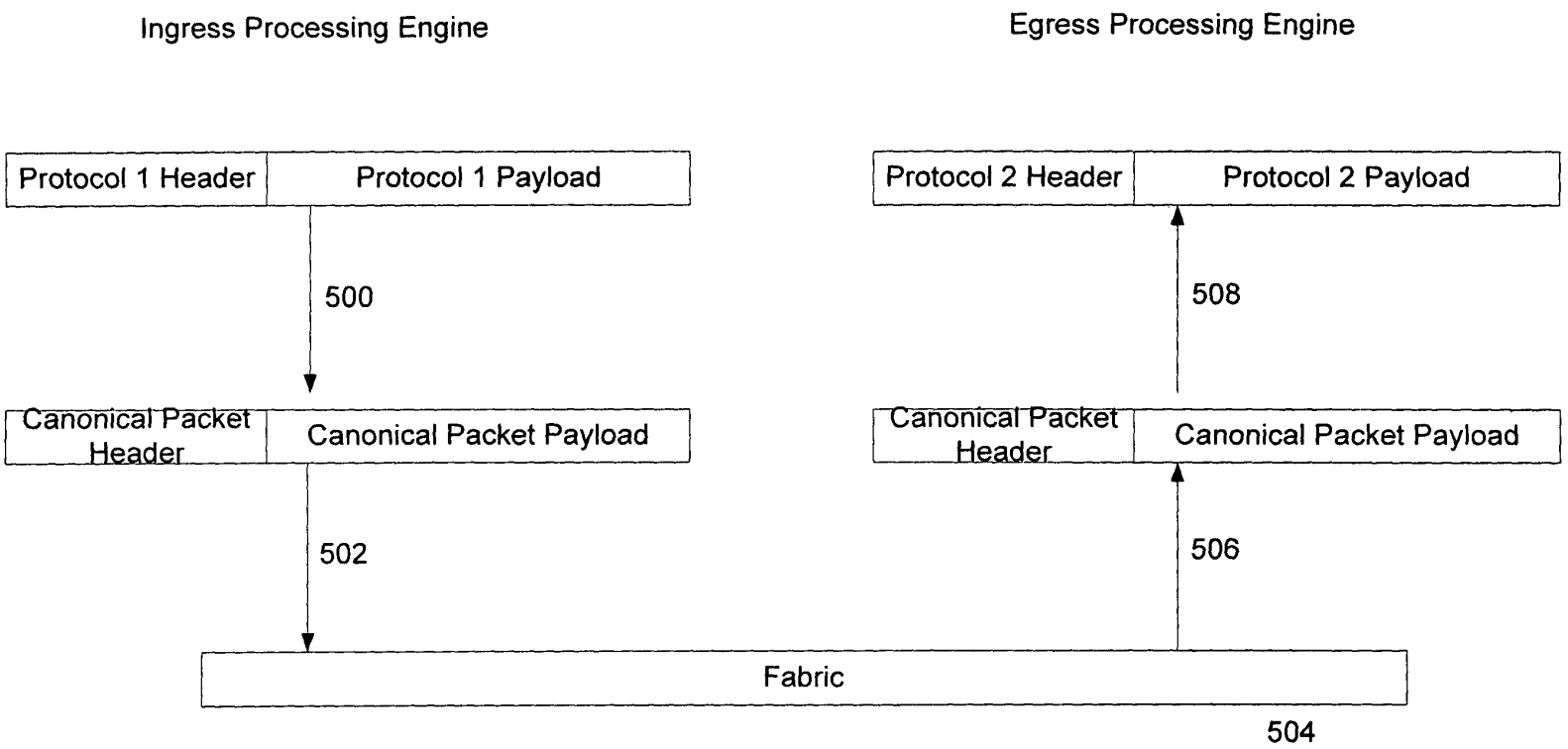
Ingress Processing Engine
Egress Processing Engine
Protocol 1 Header
Protocol 1 Payload
Protocol 2 Header
Protocol 2 Payload
500
508
Canonical Packet Header
Canonical Packet Payload
Canonical Packet Header
Canonical Packet Payload
502
506
Fabric
504

Figure 5

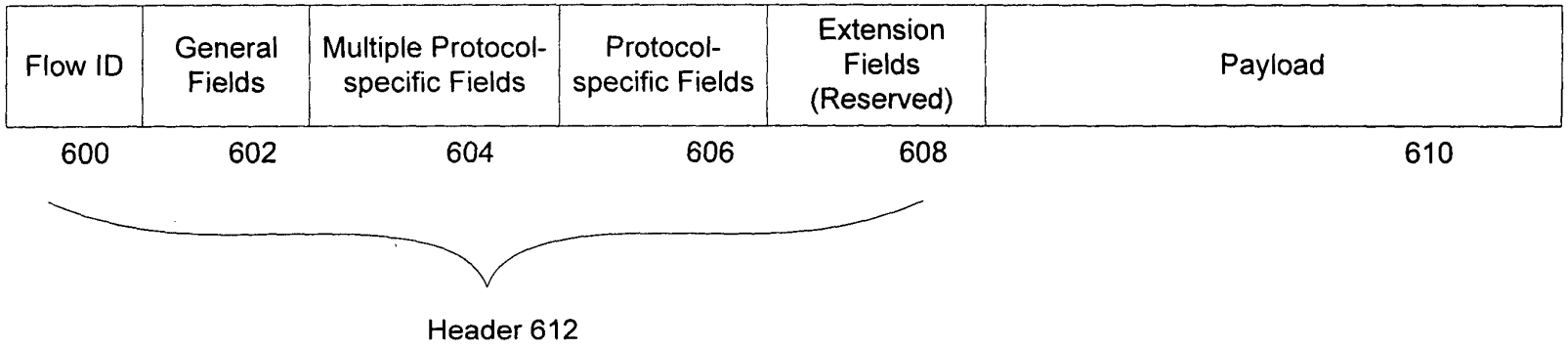

Figure 6 - Canonical Packet Format - Logical Diagram

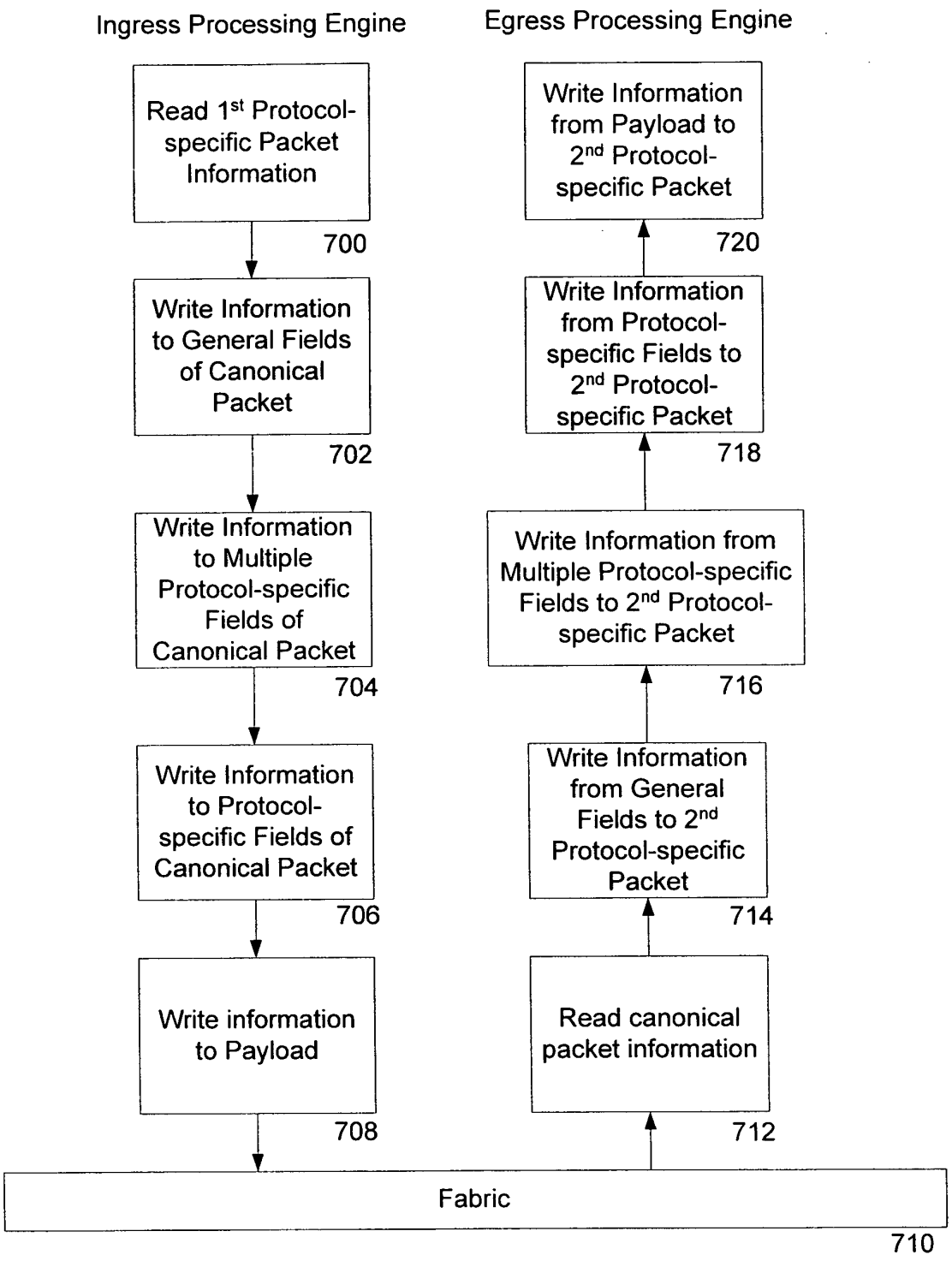
Ingress Processing Engine
Egress Processing Engine
Read 1st Protocol-specific Packet Information
700
Write Information to General Fields of Canonical Packet
702
Write Information to Multiple Protocol-specific Fields of Canonical Packet
704
Write Information to Protocol-specific Fields of Canonical Packet
706
Write information to Payload
708
Fabric
710
Read canonical packet information
712
Write Information from General Fields to 2nd Protocol-specific Packet
714
Write Information from Multiple Protocol-specific Fields to 2nd Protocol-specific Packet
716
Write Information from Protocol-specific Fields to 2nd Protocol-specific Packet
718
Write Information from Payload to 2nd Protocol-specific Packet
720

Figure 7